(A+B).(A+C) = _____

Answer (Please select your correct option)

○ B+C

○ A+BC   correct

○ AB+C

○ AC+B

Excess-8 code assigns _____ to "-8"

Answer (Please select your correct option)

○ 1110

○ 1100

○ 1000

○ 0000
correct

The output of the expression F=A.B.C will be Logic _____ when A=1, B=0, C=1.

Answer (Please select your correct option)

○ Undefined

○ One

○ Zero

                    correct

○ No Output as input is invalid.

The cell marked 6 in 4-variable K-Map represent minterm 6 or the maxterm 6 having the following binary value of variables A, B, C and D.

Answer (Please select your correct option)

○ A=1, B=1, C=0, D=0

○ A=0, B=1, C=1, D=0

correct

○ A=0, B=0, C=1, D=1

○ A=1, B=0, C=0, D=1

A comparator circuit has multiple inputs and _____ outputs

Answer (Please select your correct option)

○ 2

○ 3

           correct

○ 4

○ 7

The output A < B is set to 1 when the input combinations is _____

Answer (Please select your correct option)

○ A=10, B=01

○ A=11, B=01

○ A=01, B=01

○ A=01, B=10

correct

The _____ Encoder is used as a keypad encoder.

Answer (Please select your correct option)

○ 2-to-8 encoder

○ 4-to-16 encoder

○ BCD-to-Decimal

○ Decimal-to-BCD Priority   correct

Using multiplexer as parallel to serial converter requires _____ connected to the multiplexer

Answer (Please select your correct option)

○ A parallel to serial converter circuit

correct

○ A counter circuit

○ A BCD to Decimal decoder

○ A 2-to-8 bit decoder

The four outputs of two 4-input multiplexers, connected to form a 16-input multiplexer, are connected together through a 4-input _____ gate

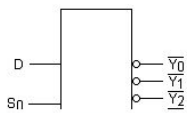**Answer (Please select your correct option)**

○ AND

○ OR correct

○ NAND

○ XOR

The device shown here is most likely a _____

```
         ┌─────┐
         │     │
  D ─────┤     │    o── Y̅0
         │     │    o── Y̅1
  Sn ────┤     │    o── Y̅2
```

Answer (Please select your correct option)

○ Comparator

○ Multiplexer

correct

○ Demultiplexer

○ Parity generator

GAL is an acronym for _____.

Answer (Please select your correct option)

○ Generic Analysis Logic

○ Giant Array Logic

○ General Array Logic

○ Generic Array Logic

correct

Made By: Waqar Siddhu

In the binary number "10011" the weight of the most significant digit is _____

Answer (Please select your correct option)

○ 2^4 (2 raise to power 4)

<p style="text-align:center;">correct</p>

○ 2^3 (2 raise to power 3)

○ 2^0 (2 raise to power 0)

○ 2^1 (2 raise to power 1)

The output of the expression F=A+B+C will be Logic _____ when A=0, B=1, C=1.

Answer (Please select your correct option)

○ Undefined

○ One

○ Zero

○ 10 (binary)   correct

Which of the following simplification method is intended to be used for Boolean expressions having more than four variables?

Answer (Please select your correct option)

○ Boolean Algebra and rules

○ Karnauph Map

○ Quine-McCluskey   correct

○ Demorgan's Theorem

A particular half-adder has _____ inputs and _____ output(s).

Answer (Please select your correct option)

○ 3, 1

○ 3, 2

○ 2, 1

○ 2, 2
                          correct   Made By: Waqar Siddhu

A quantity having continuous values is a/an _____ quantity.

Answer (Please select your correct option)

○ digital

○ binary

○ analog   correct

○ natural

Which one of the following expressions is an example of Commutative Law for Multiplication?

Answer (Please select your correct option)

○ AB+C = A+BC

○ A(B+C) = B(A+C)

○ AB=BA

correct

○ A+B=B+A

When four 1's are taken as a group on a Karnaugh map, the number of variables eliminated from the output expression is/are _____

Answer (Please select your correct option)

○ 1

○ 2

○ 3

○ 4

Identify the Boolean expression that is in standard POS form.

Answer (Please select your correct option)

○ $(A + \overline{B})(\overline{A} + B + \overline{C})$

<u>**correct**</u>

○ $\overline{A}\,\overline{B}C + A\overline{B}C$

○ $(A + \overline{B} + \overline{C})(A + B + C)$

○ $(A + B)(A + C)$

Simplifying POS (Product of sum) expression requires the grouping of _____

Answer (Please select your correct option)

○ 0s

correct

○ 1s

○ Don't care

○ Both 0s and 1s

How many input and output bits do a Half-Adder contain?

Answer (Please click here to Add Answer)

Draw the Truth-Table of NAND based S-R Latch.

Answer (Please click here to Add Answer)

Normal | Arial | 12 | B I U
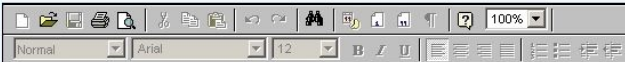
Draw the block diagram of 4-bit BCD Adder.

Answer (Please click here to Add Answer)
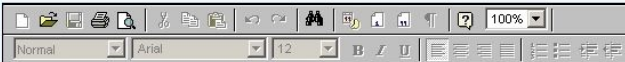
Tri-state buffer control input can be connected in four different ways. Name any three of them.

Answer (Please click here to Add Answer)

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Simplify the following Boolean expressions, using Boolean Algebraic rules

- $(A+B)(A+B')$
- $(A+B)'(A'+B')'$

Answer (Please click here to Add Answer)

Multiplexers can be used as a "Logic Function Generator". Draw only circuit diagram and function table based on three variables.

Answer (Please click here to Add Answer)